Adrian Tam

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GOAL

- To gain research experience in multiprocessor operating system

SUMMARY OF QUALIFICATIONS

- Self-motivated, innovative and able to adapt quickly
- Excellent organizational and problem solving skills
- Good communication skills with colleagues, customers and team members

TECHNICAL SKILLS

- Programming experience in **Verilog**, **VHDL**, **C/C++**, **Assembly Language** (Intel 8051, MC68000, TMS320C6000 DSP), MFC and Visual Basic
- Design Experience with **Synopysis** VMC, **Cadence** Verilog-XL, **Xilinix** Foundation Package
- Working experience with Texas Instrument TMS320C6000, Motorola MCF5206, Intel 8051 and Intel 8096 architecture
- Board Level Design Experience with Xilinx Virtex E FPGA and Motorola Coldfire Series mircoprocessor
- Familiar with NTSC, PAL, JPEG compression and USB protocol
- Strong knowledge of Unix, Windows XP and Windows 95

WORKING EXPERIENCE

Sep 03 - Present **Teaching Assistant**, University of Toronto, Toronto, Ontario
- Supervise students in Operating System and Digital System Laboratory
- Instruct students in operating system and firmware theory

May 02 – Aug 02 **Hardware Designer**, Evertz Microsystems Ltd., Burlington, Ontario
- Develop **NTSC Burst Detector** and **NTSC Pedestral Level corrector** with VHDL
- Design board level schematics for Quattro video detection board

Sep 01 – Dec 01 **Hardware Designer**, Nvidia Inc., San Jose, California
- Develop **FIFO generator** with Verilog and Perl
- Design Gaussian Filter for reference board design
- Develop hardware architectural tool to estimate area requirement

Jan 01 – Apr 01 **ASIC Designer**, Cypress Semiconductor Inc., San Diego, California
- Design test cases in **Verilog** and C to verify EZ-HOST USB microcontroller
- Develop and Optimize EZ-MOUSE-2 USB microcontroller with Verilog
- Recommended for **Outstanding Performance** in work term evaluation

May 00 – Aug 00 **PLD Software Engineer**, Cypress Semiconductor Inc., San Jose, California
- Design frequency generator configuration tool with MFC and InstallShield
- Management experience with Pin Editor project
- Recommended for Outstanding Performance in work term evaluation

Sep 99 – Dec 99 **Embedded System Developer**, NCR Canada Ltd., Waterloo, Ontario
- Develop and optimize various **image processing algorithms** in C
- Design **DMA transfer protocols** with TMS320C6201 digital signal processor
- Provide benchmark measurements for JPEG compression

Jan 99 – Apr 99 **Program Designer,** Nortel Networks Inc., Toronto, Ontario
- Design and implement testing procedures for Meridian Mail System
- Improved the reliability of Meridian Mail System by developing software upgrades in Pascal

Sep 98 – Dec 98 **Undergraduate Research Assistant,** University of Waterloo
- Develop a robotic control system using Visual Basic 5.0
- Design and implement new object-oriented programming codes

EDUCATION

2002 - present **University of Toronto**, Toronto, Ontario
- Candidate for Master of Applied Science
- Courses in operating system, computer architecture and runtime optimization

1997 – 2002 **University of Waterloo**, Waterloo, Ontario
- Bachelor of Applied Science with Distinction, Computer Engineering Co-Op
- Courses in VHDL, operating system, and computer architecture with average over 85%

AWARDS

- **University of Toronto's Graduate Fellowship**
- Term's **Dean Honour List** for 2B study term

EXTRA CURRICULAR ACTIVITIES

2002 Member of the Operating System Reading Group
2002 Soccer Referee, Nortel Student Soccer League
1996 – 1997 Member of the Computer Science Team, Father Michael Goetz Secondary
1997 Captain of Math League, Father Michael Goetz Secondary School
1997 Volunteer Host, Inter-cultural Neighbourhood Social Service
- Assisted new immigrants to adapt living in Canada

REFERENCE

- Available upon request