



Institute of Electrical and
Electronics Engineers



IEEE Computer Society



TC on Multiple-Valued
Logic



University of Toronto

34th INTERNATIONAL SYMPOSIUM ON MULTIPLE-VALUED LOGIC

University of Toronto, Canada

May 19-22, 2004

CALL FOR PAPERS

The Multiple-Valued Logic Technical Committee of the IEEE Computer Society will hold its 34th annual symposium on May 19-22, 2004 in Toronto, Canada. The symposium is sponsored by the IEEE Computer Society and the University of Toronto. You are invited to submit an original paper, survey or tutorial paper on any subject in the area of multiple-valued logic, including but not limited to:

- Algebra and Formal Aspects
- Logic Design and Switching Theory
- Fault Detection and Diagnosis
- Spectral Techniques
- Circuit/Device Implementation
- Nano Technology
- Quantum Computing
- Automatic Reasoning
- Fuzzy Logic and Soft Computing
- Logic Programming
- Data Bases, Data Mining, Web Intelligence
- Reliability and Safety
- Computer Security
- Philosophical Aspects

Authors should submit papers by **November 1, 2003**. Electronic submissions by PDF or PS files are strongly preferred. In exceptional cases, submissions of four (double-spaced typed) paper copies are also acceptable. Each manuscript should include a 50-100 word abstract and should not exceed 6 pages in the proceedings format. Authors will be notified about acceptance by **February 1, 2004**. Photo-ready copies of accepted papers are due on **March 1, 2004**.

For submission details, visit: <http://www.eecg.utoronto.ca/~ismvl2004>

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The symposium will be preceded by the Post Binary ULSI Workshop on May 19, 2004.