University of Toronto
Department of Electrical and Computer Engineering

ECE 241F — DIGITAL SYSTEMS
Laboratory and Tutorial Schedule
Fall 1996

All labs are held in SF2201.
Tutorials will be held in SF2201 and SF2204.

P0101	Tuesday	3pm–6pm
P0102	Wednesday	9am–12noon
P0103	Thursday	9am–12noon
P0104	Friday	3pm–6pm

IMPORTANT: To avoid spending valuable tutorial time organizing groups in the first meeting and to help in our planning, please sign up for a tutorial slot on the lists posted outside SF2201 before next Monday (Sept. 9) at 9am.

Only 12–14 people can fit into one tutorial group, so do not try to enter any group that has been filled up.

Week of	
Sept. 9	Tutorial 1 – Design process, Venn diagrams, basic logic gates
Sept. 16	Tutorial 2 – Two-level combinational logic and optimization
Sept. 23	Lab 1 - Building SSI (Prep done with Logic Works)
Sept. 30	Lab 2 - Ring oscillator, counters, measure gate delay
Oct. 7	Tutorial 3 - Optimization and flip flops
Oct. 14	Lab 3 - ALU using SSI
Oct. 21	Lab 4 - PAL Tutorial/Intro + Lab assignment
Oct. 28	Tutorial 4 (midterm week - November 6, 6-8pm)
Nov. 4	Lab 5 - Bigger ALU using PALs + Lab assignment
Nov. 11	Lab 6 - Project Week 1
Nov. 18	Lab 7 - Project Week 2
Nov. 25	Lab 8 - Project Week 3