

ECE241F - Digital Systems - Basic Course Information

Fall 1999

A. Carusone, P. Lehn, J. Rose

Instructor and Lecture Information

Section	Section 3		Section 1		Section 2	
Instructor:	Jonathan Rose		Peter Lehn		Anthony Carusone	
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Office Hours	please contact by email or during lectures to make appointment					
Lecture Rooms	Tues 9-10	SF 1101	Tues 3-4	MC 252	Mon 3-4	MC 252
	Wed 2-3	MC 252	Thurs 2-3	SF 1105	Tues 10-11	TBA
	Fri 10-11	SF 1101	Fri 3-4	MC 252	Fri 9-10	MC 252

Grading

Labs	10%	Midterm	25%	Project	10%	Exam	55%
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Midterm

Date, Time and , Room TBA. This is an early mid-term! Closed book.

Exam

Type D - examiner specified aids, and no calculators of any kind permitted.

Text

Fundamentals of Digital Logic with VHDL Design, Stephen Brown and Zvonko Vranesic.
Published by McGraw-Hill, ISBN 0-07-012591-0. Book contains software required in course.

On-Line Documents

<http://www.eecg.toronto.edu/~jayar/courses/ece241F/home.html>