

Corrections to “6-k Ω 43-Gb/s Differential Transimpedance-Limiting Amplifier With Auto-Zero Feedback and High Dynamic Range”

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In the above paper [1], Fig. 2 appeared incorrectly. The correct figure follows. We regret the error.

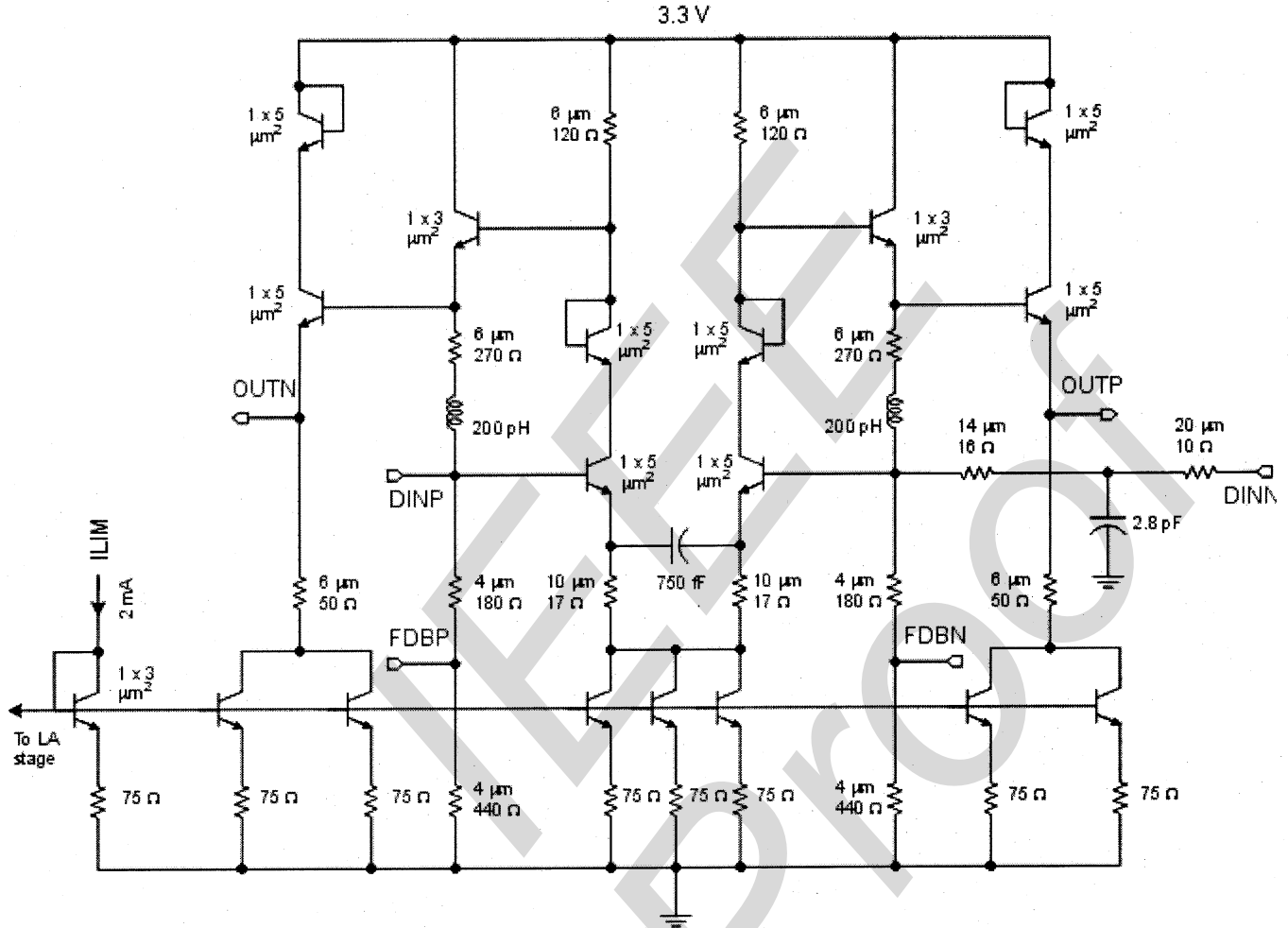


Fig. 2. Differential TIA stage schematic.

REFERENCES

- [1] H. Tran, F. Pera, D. S. McPherson, D. Viorel, and S. P. Voinigescu, “6-k Ω 43-Gb/s differential transimpedance-limiting amplifier with auto-zero feedback and high dynamic range,” *IEEE J. Solid-State Circuits*, vol. 39, pp. 1680–1689, Oct. 2004.

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