



W04: Submillimetre-wave Monolithic Integrated Circuits

SiGe HBT Based S-MMICs

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The 42nd European Microwave Conference





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- Jaro Pristupa and CMC for CAD and support



- Motivation
- Technologies
- 120-123 GHz Distance Sensor with above IC Antenna
- 150-170 GHz Transceiver with 3 on Die Antennas
- 143-150 GHz SUCCESS Sensor with BIST
- Potential Solutions for G-Band Sensors

Applications

Industrial range and Doppler

sensors



 Active/passive imagers for security and remote sensing

 100+Gb/s short-range reconfigurable wireless data transmission in the data center



Wish list for submillimeter radio/radar

- P_{TX} = 10 dBm
- PN < -90 dBc/Hz in band, <-140 dBc/Hz beyond 50 MHz
- NF < 12 dB
- P_{DC} < 1W
- BW > 20% (for passive imaging and 100+Gbs radio)
- QPSK, 16/64 QAM modulation schemes
- 50-100 Gb/s digital streams in TX
- 100 GS/s 8-10 bit ADC in receiver



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STMicroelectronics' 130-nm SiGe BiCMOS



CMOS, SiGe, ISS µ-strip lines: D-Band



SiGe vs. ISS µ-strip lines: H-Band



Si Transistor Performance at G-Band



SiGe HBT: BC9MW vs. B5T





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Dual Receive Channel Transceiver



Push-Push 148-170 GHz VCO



 $P_{DC} = 360 \text{ mW}, P_{OUT} = -10 \text{ dBm}, PN = -82 \text{ dBc/Hz}$ at 1 MHz offset

148-170 GHz LO Tree and TX Amplifiers





3.0 V

3.0 V

e



 $P_{\rm D} = 126 \text{ mW}$



148-170 GHz Low Noise Amplifier



 $P_{D} = 67 \text{ mW}$, Gain= 20 dB, NF < 12 dB.

Die photograph



Chip: 2.1mm×2.9mm

130-nm BiCMOS9MW: SiGe HBT f_T = 230 GHz, f_{MAX} = 280 GHz

RX Breakout Gain and Noise Figure



Transceiver PLL Phase Noise



On-die Doppler Test





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Layout and performance summary

- BiCMOS9MW, $f_T/f_{MAX} = 230/280 \text{ GHz}$
- Tuning range 143-152 GHz
- NF<10 dB, Pout >-6 dBm
- PN < -83 dBc/Hz at 1MHz</p>
- P_{DC} = 800 mW
- Highest fundamental frequency transceiver



2.6mmx2.3mm



150-GHz Dynamic Divide-by-64



- Measured in test structure with 150-GHz VCO
- 1.8V/52mA + 1.2V/3mA= 97.2 mW + 25.2 mW in 50Ω buffer



Coupler with detectors





Coupler with detectors: Linearity





Digital Tuner States





145 GHz Fundamental Frequency VCO



•143-152 GHz tuning range
•PN=-103dBc/Hz @10MHz
•P_{DC} = 72 mW



Measured output power



Power at antenna port measured with ELVA power sensor
On-chip and external measurements track very well



Receiver schematics





Receiver Gain and Noise Figure



13-15 dB gain, 23 dB of gain control in LNA Low noise figure: 8.5-10.5dB



Antenna and die in package



•QFN package with bondwire transition to antenna on alumina

Courtesy of Robert Bosch GmbH. Karlsruhe Institure of Technology and EU SUCCESS project partners



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240-GHz Transceiver Blocks



240-GHz Amplifier



Measurements vs. sims



VCO Topology & Design



150-GHz VCO [Balteanu et al. IMS2012]

Topology

- Colpitts-Clapp for tuning [Pohl et al., CSICS 2008]
- $L_{c} \approx L_{B}$ for best performance
- L for bias noise filtering
- Colpitts above 200 GHz
- No MiM cap across BE junction
- HBT varactors (no AMOS)

Design (at 125 °C)

- Textbook: Based on small-signal sims of Z(f)
- Large signal verification

Multiplier Design Methodology



240-GHz VCO



[A. Tomkins et al, BCTM 2012]



300-GHz VCO+Buffer+Doubler







200+ GHz Signal Source Test Methodology

• S-parameters for $f_{0SC}(V)$ from regions where $S_{11}>0$ dB

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[K. Yau et al., IEEE Micro, Feb. 2012]
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- VNA downconverter to 300 MHz IF and PSA for tuning range and phase noise
- Erikson calorimeter for P_{out} vs. control voltage

Tuning Range: S-Params/Spectrum



240-GHz VCO Phase Noise and Pout



150/240 GHz VCO Comparison



300-GHz Signal Source Comparison



300-GHz Signal Source Comparison (ii)



Phase Noise of VCO-doubler at 309 GHz



300-GHz vs. 150-GHz Phase Noise



SAME VCO in BOTH!

480-GHz source: VCO+doubler



Conclusions

- D-Band Push-push & fundamental frequency transceivers
- On-die and in-package antennas
- 240-GHz fundamental VCO in SiGe
- Lowest phase noise 300-GHz signal source
- Buffer between VCO and doubler is critical
- 240-300 GHz transceiver feasible in 55-nm SiGe BiCMOS

Why 100-300 GHz?

Sorin Voinigescu, October 28, 2012

- Silicon transistors with f_{MAX}>400 GHz
- Smaller wavelengths: high res, high BW
- Small antenna size with good gain
- Lower power LNA, mixer, receiver
- But...
 - higher power PLL,
 - ◆ reduced P_{out},
 - shorter range ~1/f² or ~1/f⁴





Source: G. Rebeiz UCSD

Technology: 280/400 GHz SiGe HBT



SiGe HBT Model Verification to 325 GHz





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120-GHz Distance Sensor



[I. Sarkas et al. Trans. MTT, March 2012]

Layout and Packaging



Chip: 2.2mm×2.6mmPackage: $7mm\times7mm$ Dr. J. Hasch130-nm BiCMOS9MW: SiGe HBT f_T = 230 GHz, f_{MAX} = 280 GHz

Measured Output Power above Antenna

[Courtesy of Dr. J. Hasch Robert Bosch GmbH]

ADDITSU POWER METER ML4803

150-GHz VCO-prescaler



150-GHz VCO Synthesizer Measurements



Packaging: QFN & wirebonding



[Courtesy of Dr. J. Hasch Robert Bosch GmbH]

Sorin Voinigescu, October 28, 2012

In-package antennas simulation



Measured Pout and Phase Noise 300-GHz source: VCO+buffer+doubler

