







Protected (privileged) Instructions

- What are protected instructions?
 - Who gets to execute them?
 - How does the CPU know whether they can be executed?

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- Difference between user and kernel mode
- Why do they need to be privileged?
- What do they manipulate?
 - Protected control registers
 - Memory management
 - I/O devices

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Events Synchronous: fault (exceptions), system calls Asynchronous: interrupts, software interrupt What are faults, and how are they handled? What are system calls, and how are they handled? What are interrupts, and how are they handled? What are interrupts, and how are they handled? How do I/O devices use interrupts? Timer interrupt, why? What is the difference between exceptions and interrupts?













Mutual Exclusion

- What is mutual exclusion?
- What is a critical region?
 - What guarantees do critical region provide?
 - What are the requirements of critical regions?
 - Mutual exclusion (safety)
 - Progress (liveness)

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- Bounded waiting (no starvation: liveness)
- No assumption of the CPU speed/number

• What are the mechanisms for building critical regions?

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· Locks, semaphores, monitors, condition variables





- What is a semaphore?
 - What does P do?
 - What does V do?
 - How does a semaphore differ from a lock?
 - What is the difference between a binary semaphore and a counting semaphore?

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- When do threads block on semaphores?
- When are they woken up again?
- Using semaphores to solve synchronization problems
 - Readers/Writers problem
 - Bounded Buffers problem

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- What is a condition variable used for?
 - Coordinating the execution of threads
 - Not mutual exclusion
- Operations

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- What are the semantics of Wait?
- What are the semantics of Signal?
- What are the semantics of Broadcast?
- How are condition variables different from semaphores?

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